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| Contains Information about high level software requirements of the system being created |

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# Change History

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| 1 | S.C | 10/10/2017 | Initial Version | Yes |

# Referenced Documents

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| N/A |  |  |  |  |

# 1 Introduction

## 1.1 Purpose

This Sub-system provides the software requirements for the validation and selection of the Main Missile System Checks parameters for use in Maintenance of the system. This document describes the overall architecture of the software and provided information on how that architecture has links to the Ada implementation. Traceability has been provided for the generated code packages and operations.

## 1.2 Scope

This Software Requirement Specification captures the system design for the System-Check. The System Checks software will integrate with the flight controller software. As such, it is required to interface with certain customer provided packages. The design identifies these packages and identifies the parts of those packages used, but does not describe them in detail as their design is not under the wing of this development arm.

## 1.3 Overview

The remainder of this document briefly gives an overview of the system to which the System-Checks function relates, and an overall description of how it works followed by the detailed requirements to be implemented.

# 2 Software Design

## 2.1 System Overview

The system intends to perform Main\_Systems\_Checks, First\_Phase, Second\_Phase, Third\_Phase and Fourth\_Phase on the ADLAS Computer. The hardware provides a number of registers containing information about the system and the status of these registers is checked to determine the status of the system.

The tests are generally performed by reading the required bits of a number of hardware registers. Some registers react to values contained in other registers and so to test them it is necessary to write to a given register and then read the associated register to check its value has changed as expected.

### 2.1.1 Main System Checks

Main\_Systems\_Checks tests run as a uninterrupted sequence of tests, although prior to executing of the tests, a copy of the hardware registers is created and the test is performed on the register copies.

### 2.1.2 First\_Phase

There is a single First\_Phase test consisting of a number of tests and which is run when instructed by the operator. Again, First\_Phase runs as a sequence of uninterrupted tests. Prior to executing the tests, a copy of the hardware registers is made and the test performed on the register copies.

### 2.1.3 Second\_Phase

There is a single Second\_Phase consisting a number of simulator tests

## 2.2 Design Approach

The approach used to represent the design will be based on Booch Object Oriented Design whereby the design is split into a number of high level ‘objects’ which can be subsequently decomposed individually into one or more further objects or one or more Ada packages with minimal reference to one another. The ‘objects’ will not be implemented but are used as a device to encapsulate the design. The overall layout of the objects is as follows:

# 3 Software Requirements